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	Filing Date	July 14, 2003
	First Named Inventor	David Mark
	Examiner Name	Jermele M. Hollington
	Art Unit	2829
	Patent No.	
Attorney Docket Number		X-1269-1P US

ENCLOSURES (check all that apply)		
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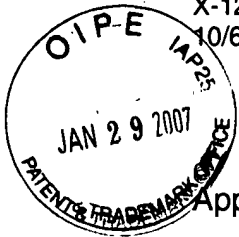
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X-1269-1P US  
10/619,136

PATENT  
Conf. No. 6066



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: David Mark et al.

Assignee: Xilinx, Inc.

Title: Method for Gross I/O Functional Test At Wafer Sort

Serial No.: 10/619,136

Filing Date: 07/14/2003

Examiner: Jermele M. Hollington

Art Unit: 2829

Docket No.: X-1269-1P US

Conf. No.: 6066

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Mail Stop Amendment  
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AMENDMENT IN RESPONSE TO SIXTH OFFICE ACTION

In response to the Office Action mailed from the Patent Office on  
November 16, 2006, Applicants submit the following amendments and remarks.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks** begin on page 7 of this paper.